1/18

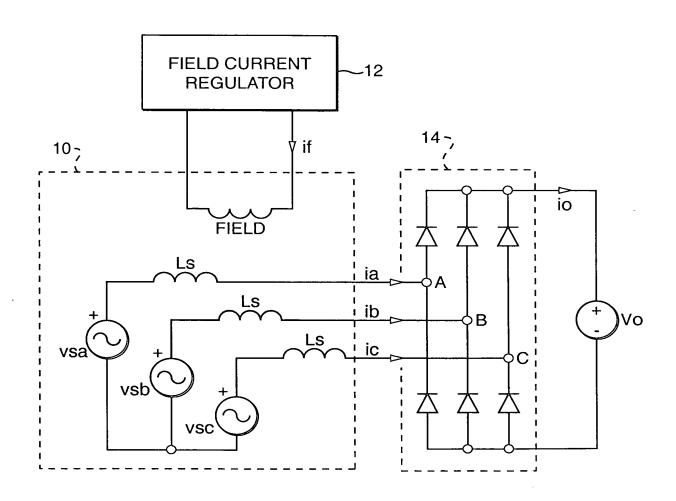
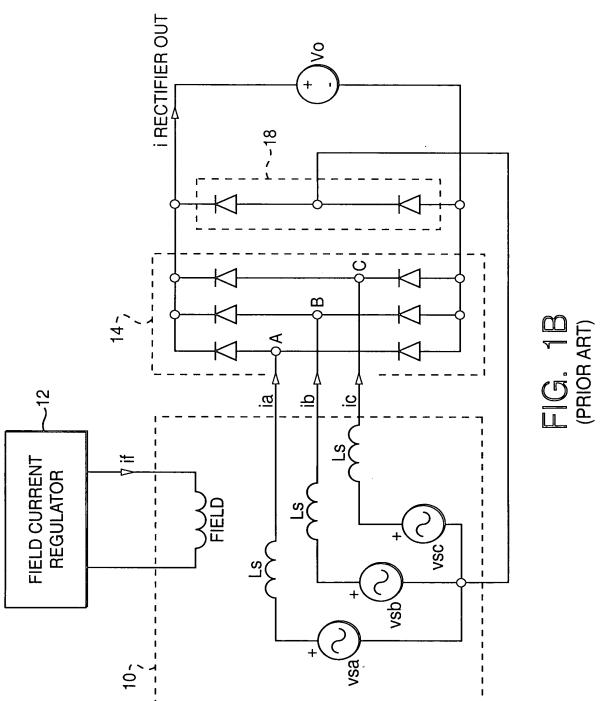
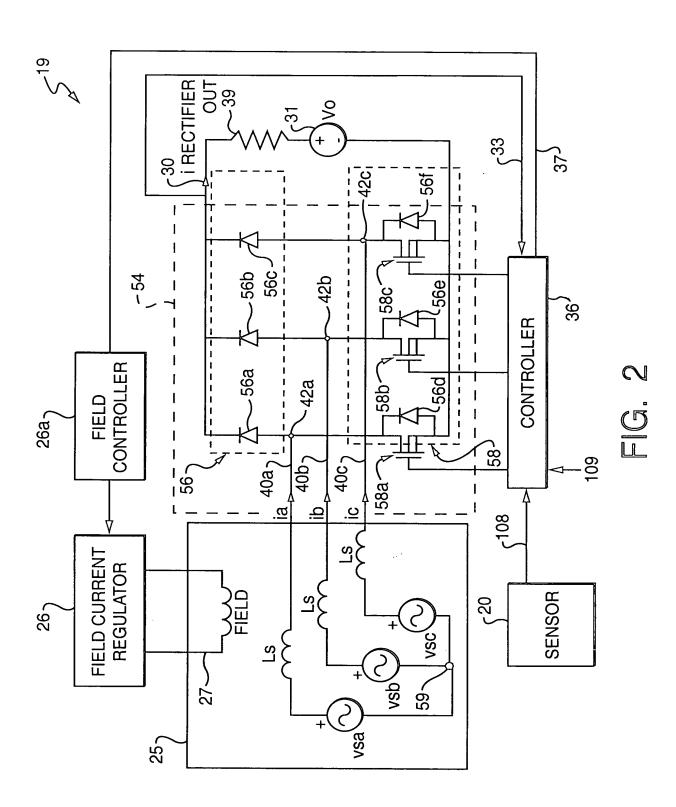


FIG. 1A (PRIOR ART)

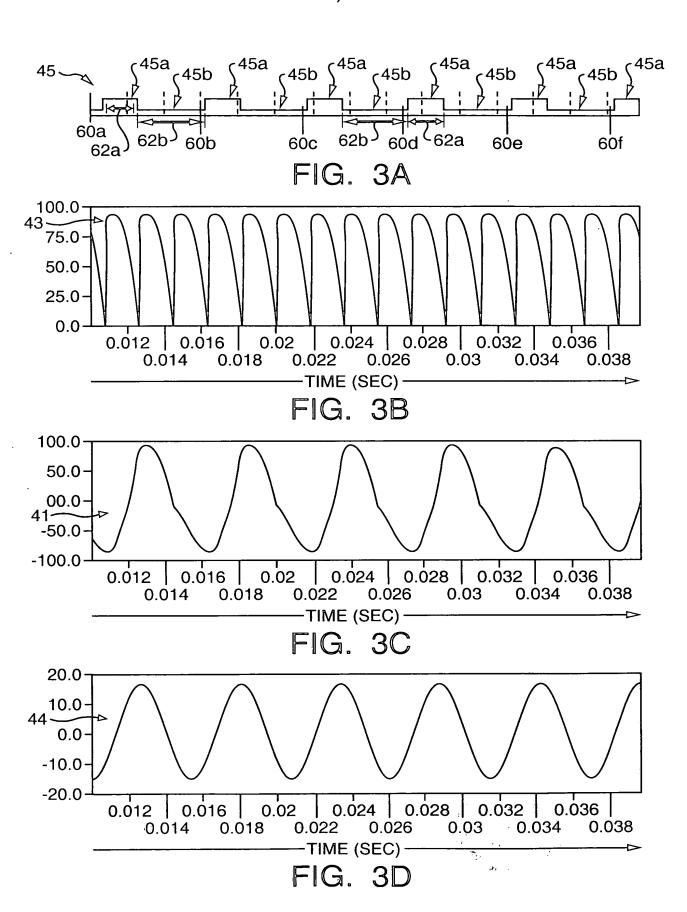
2/18



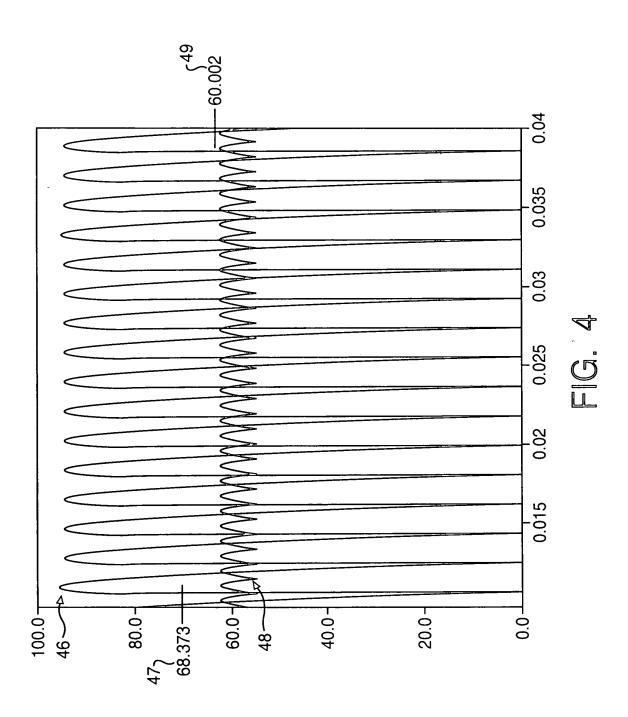
3/18



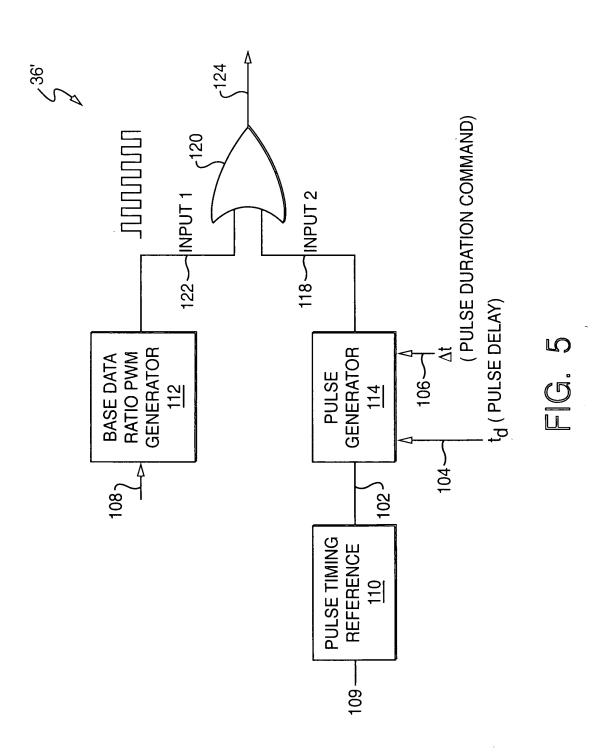
4/18



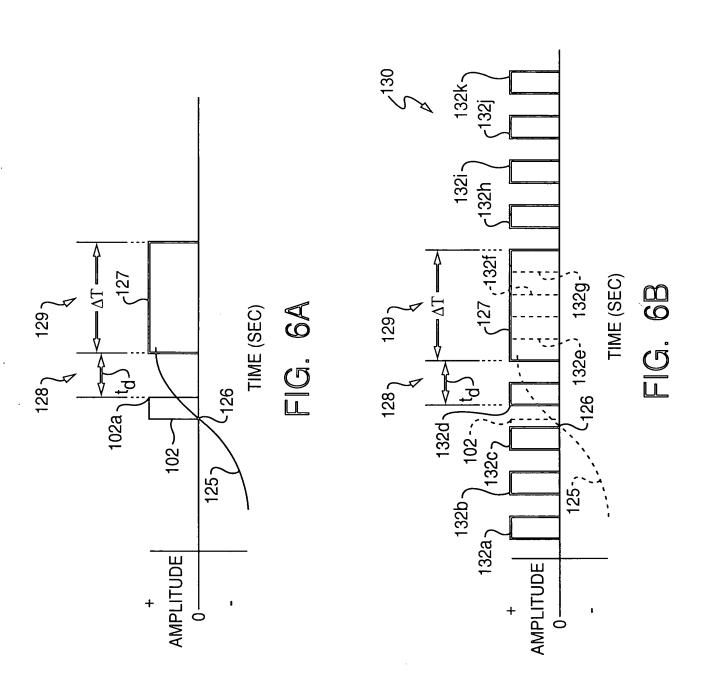
5/18



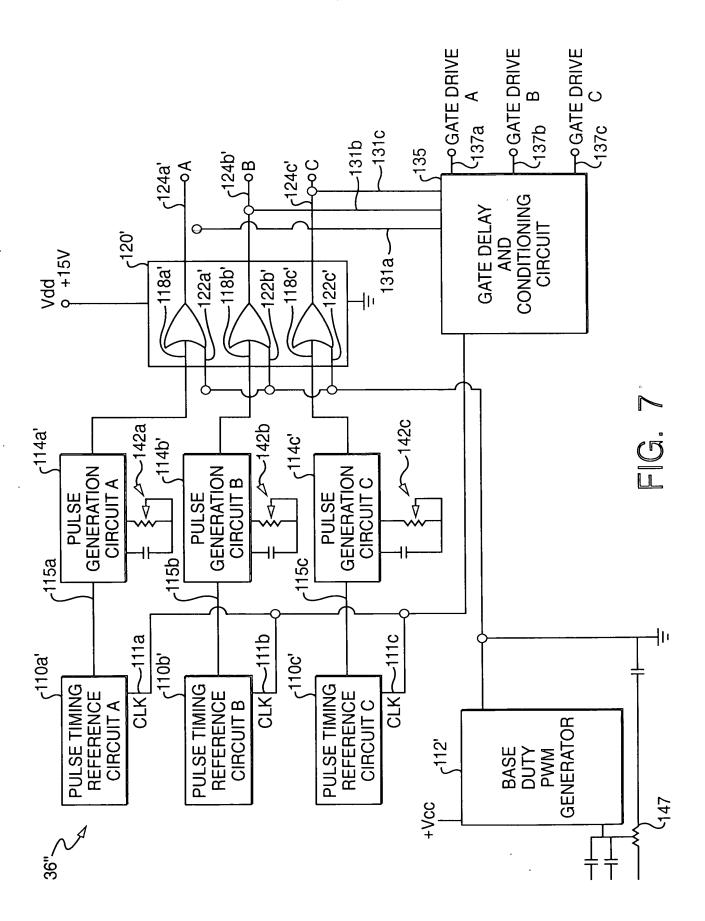
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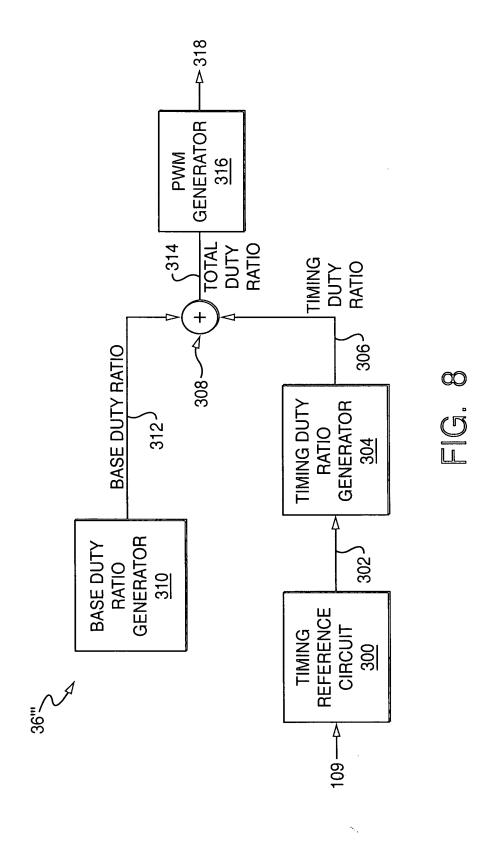
7/18

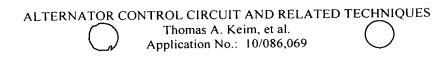


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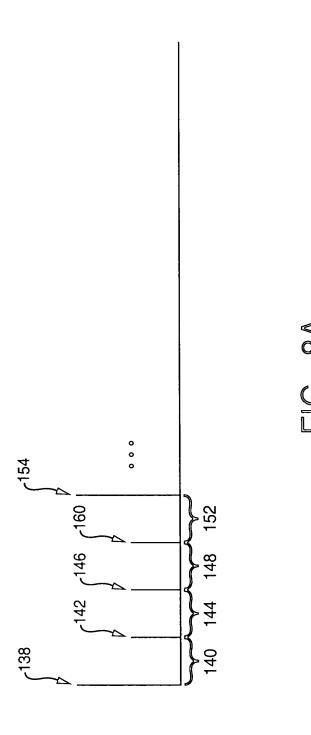


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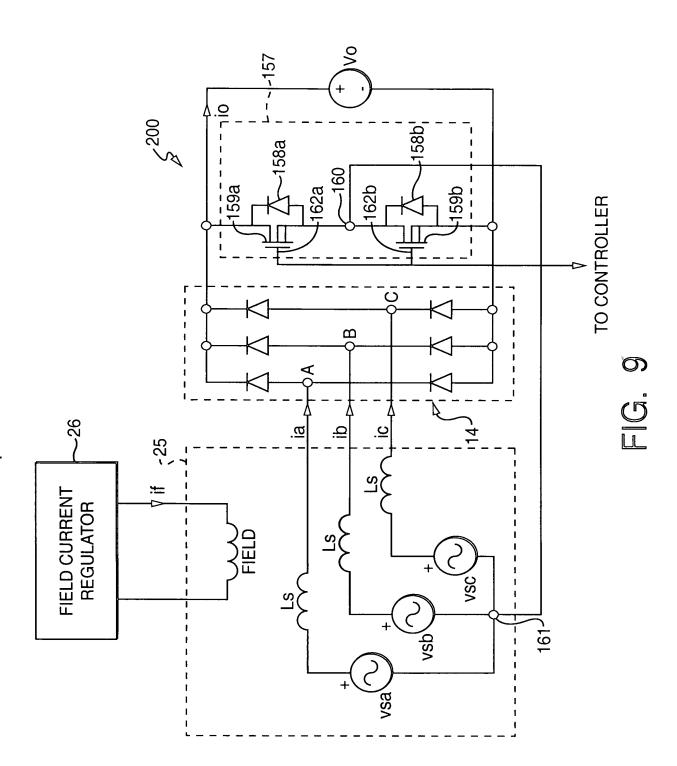




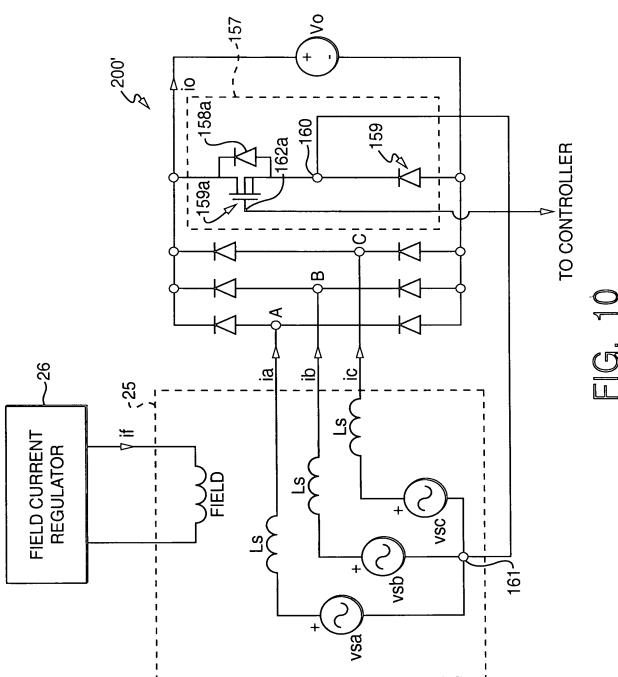
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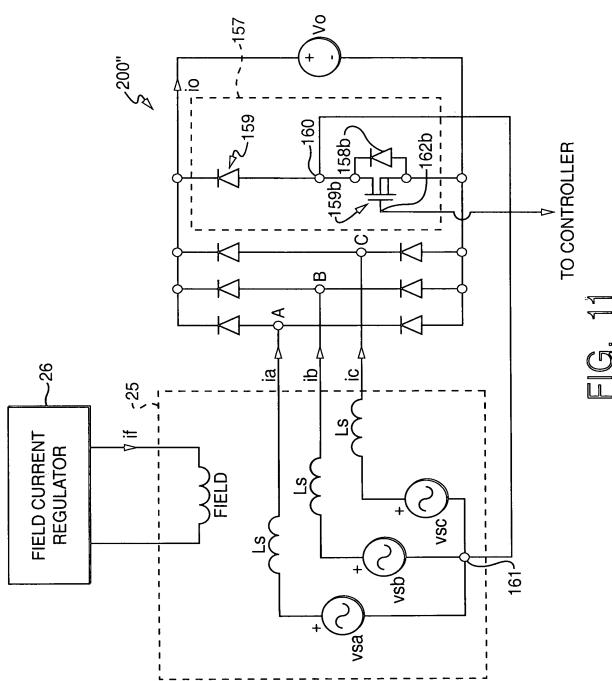
11/18



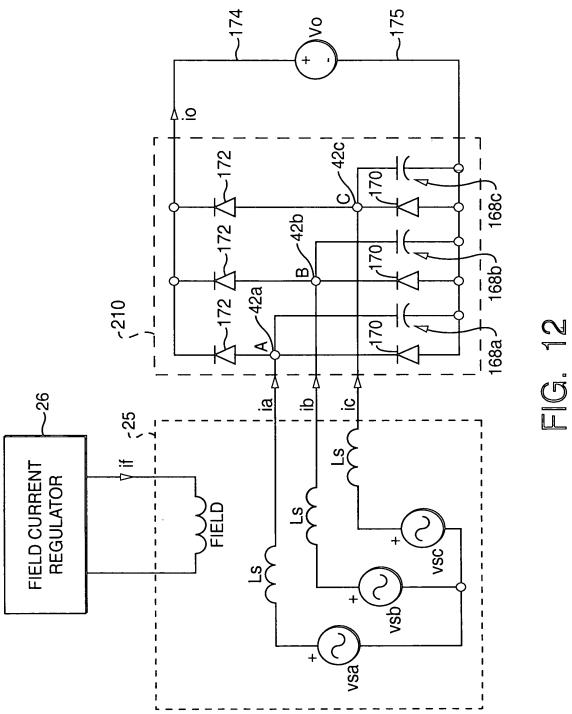
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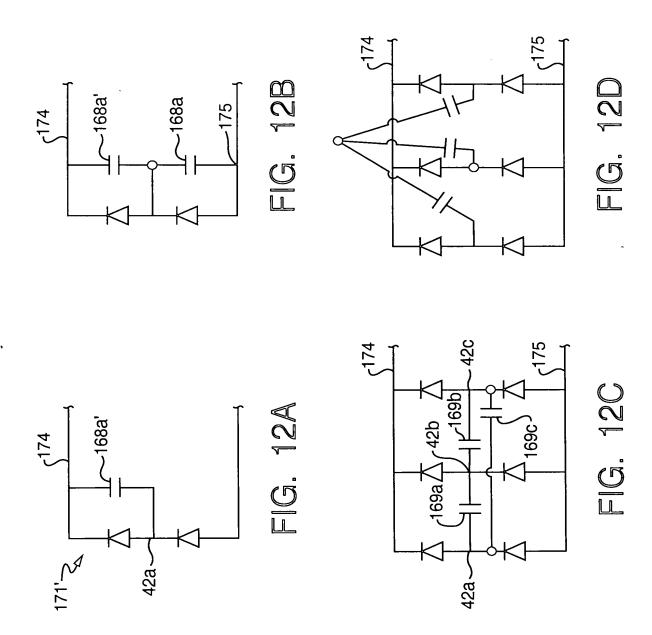
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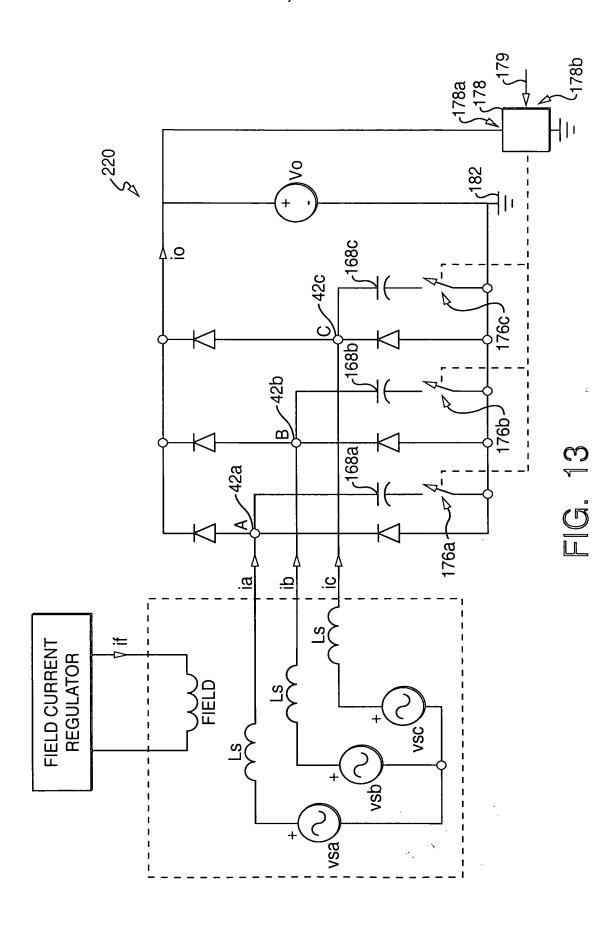
ALTERNATOR CONTROL CIRCUIT AND RELATED TECHNIQUES

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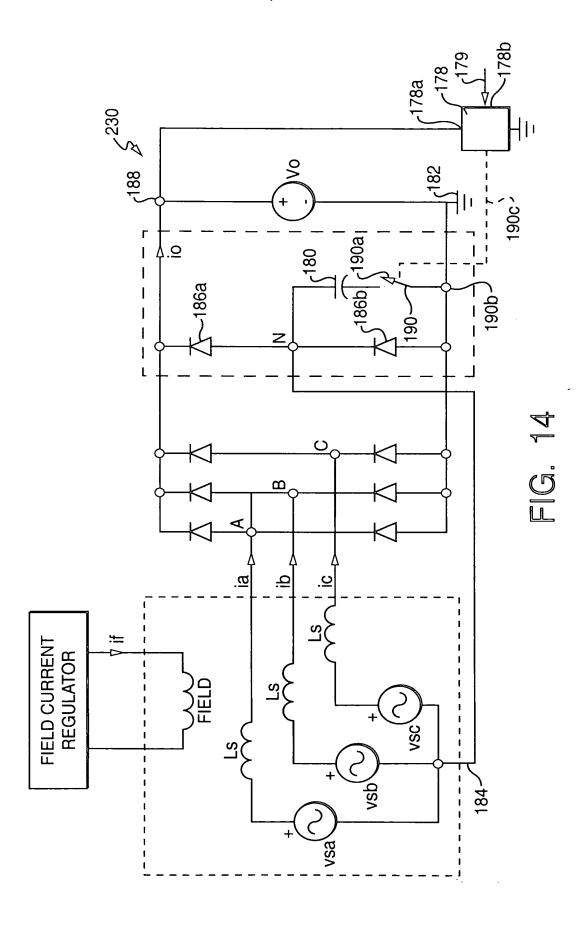
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